

10704045

DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My post office address, residence address and country of citizenship as stated below next to my name are true and correct;

I believe I am an original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled "Semiconductor Device Testing Apparatus and a Test Tray for Use in the Testing Apparatus," described in international patent application number PCT/JP 98/02979 filed July 2, 1998;

I have reviewed and understand the contents of the above identified application, including the description, claims and drawings; and

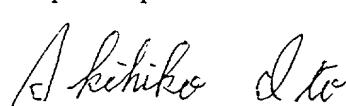
I acknowledge the duty to disclose information known to be material to patentability as defined in Title 37, Code of Federal Regulations § 1.56.

I claim benefit of priority under 35 U.S.C. § 119(a)-(d) or § 365(b) for application number 176762/97 filed July 2, 1997 in Japan.

I declare that no foreign application for patent or inventor's certificate and no international patent application has been filed on the same subject matter prior to the earliest-filed application upon which priority is claimed.

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Inventor: Akihiko Ito
Post Office: 5-2-35-201, Higashi, Hanyu-shi,
Saitama 348-0052 Japan
Residence: Hanyu-shi, Saitama, Japan
Citizenship: Japan



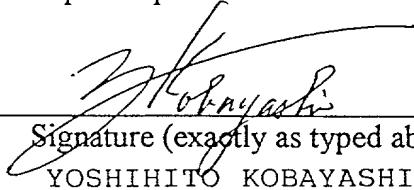
Signature (exactly as typed above)

AKIHIKO ITO

February 2, 1999

Date

Inventor: Yoshihito Kobayashi
Post Office: 1-11-6, Sakuracho, Gyoda-shi,
Saitama 361-0022 Japan
Residence: Gyoda-shi, Saitama, Japan
Citizenship: Japan



Signature (exactly as typed above)

YOSHIHITO KOBAYASHI

February 2, 1999

Date

Inventor: Yoshiyuki Masuo
Post Office: 4-5-405, Shimizicho, Gyoda-shi,
Saitama 361-0047 Japan
Residence: Gyoda-shi, Saitama, Japan
Citizenship: Japan

Yoshiyuki Masuo
Signature (exactly as typed above)
YOSHIYUKI MASUO

February 2, 1999

Date

Inventor: Tsuyoshi Yamashita
Post Office: 4911-A-201, Nakano, Oramachi,
Ora-gun, Gunma 370-0603 Japan
Residence: Ora-gun, Gunma, Japan
Citizenship: Japan

TSUYOSHI YAMASHITA
Signature (exactly as typed above)
TSUYOSHI YAMASHITA

February 2, 1999

Date

PATENT
Docket No. 333772000101

CERTIFICATE OF MAILING BY "EXPRESS MAIL"

Express Mail Label No.: EL45362049745 Date of Deposit: Sept. 25, 2001

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on the date indicated above and is addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231.

Ararat Kapouyian
Ararat Kapouyian

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

ITO et al.

Serial No.: To Be Assigned

Filing Date: Herewith

For: SEMICONDUCTOR DEVICE TESTING
APPARATUS AND A TEST TRAY FOR
USE IN A TESTING APPARATUS

Examiner: Unassigned

Group Art Unit: Unassigned

REVOCATION OF PRIOR POWER OF ATTORNEY AND
POWER OF ATTORNEY AND PROSECUTION BY ASSIGNEE
UNDER 37 C.F.R. § 3.71

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

ADVANTEST CORPORATION, the assignee of the entire right, title and interest in this patent/reexamination application, hereby revoke all Powers of Attorney previously granted relating to this application and appoint as its attorneys or agents, with full power of substitution, association, and revocation, to prosecute this application and to transact all business in the United States Patent and Trademark Office connected herewith:

Lisa A. Amii (Reg No. 48,199)
Mehran Arjomand (Reg No. P48,231)
Sanjay S. Bagade (Reg No. 42,280)
Shantanu Basu (Reg No. 43,318)
Vincent J. Belusko (Reg No. 30,820)
Kimberly A. Bolin (Reg No. 44,546)
Tyler S. Brown (Reg No. 36,465)

Randolph Ted Apple (Reg No. 36,429)
Laurie A. Axford (Reg No. 35,053)
Erwin J. Basinski (Reg No. 34,773)
Richard R. Batt (Reg No. 43,485)
Jonathan Bockman (Reg No. 45,640)
Barry E. Bretschneider (Reg No. 28,055)
Nicholas Buffinger (Reg No. 39,124)

P R O C E S S I N G
U. S. P. T. & T. O.

A. Randall Camacho (Reg No. 46,595)	Mark R. Carter (Reg No. 39,131)
Robert K. Cerpa (Reg No. 39,933)	Peng Chen (Reg No. 43,543)
Alex Chartove (Reg No. 31,942)	Thomas Chuang (Reg No. 44,616)
Thomas E. Ciotti (Reg No. 21,013)	Cara M. Coburn (Reg No. 46,631)
Matthew M. D'Amore (Reg No. 42,457)	Raj S. Davé (Reg No. 42,465)
Peter Davis (Reg No. 36,119)	Karen B. Dow (Reg No. 29,684)
Stephen C. Durant (Reg No. 31,506)	Carolyn A. Favorito (Reg No. 39,183)
David L. Fehrman (Reg No. 28,600)	Hector Gallegos (Reg No. 40,614)
Thomas George (Reg No. 45,740)	Debra J. Glaister (Reg No. 33,888)
Kenneth R. Glick (Reg No. 28,612)	Bruce D. Grant (Reg No. 47,608)
Johney U. Han (Reg No. 45,565)	Douglas G. Hodder (Reg No. 41,840)
Alan S. Hodes (Reg No. 38,185)	Charles D. Holland (Reg No. 35,196)
Kelvan P. Howard (Reg No. P48,999)	Peter Hsieh (Reg No. 44,780)
Jill A. Jacobson (Reg No. 40,030)	Wayne Jaeschke, Jr. (Reg No. 38,503)
Madeline I. Johnston (Reg No. 36,174)	Parisa Jorjani (Reg No. 46,813)
Ararat Kapouytian (Reg No. 40,044)	Richard C. Kim (Reg No. 40,046)
Cameron A. King (Reg No. 41,897)	Lawrence B. Kong (Reg No. P49,043)
Kawai Lau (Reg No. 44,461)	Glenn Kubota (Reg No. 44,197)
Rimas T. Lukas (Reg No. 46,451)	Michael J. Mauriel (Reg No. 44,226)
Gladys H. Monroy (Reg No. 32,430)	Philip A. Morin (Reg No. P-45,926)
Kate H. Murashige (Reg No. 29,959)	Paul S. Naik (Reg No. P49,075)
Mabel Ng (Reg No. P48,922)	Martin M. Noonan (Reg No. 44,264)
Catherine M. Polizzi (Reg No. 40,130)	Phillip Reilly (Reg No. 41,415)
Debra A. Shetka (Reg No. 33,309)	Terri Shieh-Newton (Reg No. 47,081)
Rebecca Shortle (Reg No. 47,083)	Kevin R. Spivak (Reg No. 43,148)
Stanley H. Thompson (Reg No. 45,160)	Thomas L. Treffert (Reg No. P48,279)
Brenda J. Wallach (Reg No. 45,193)	Michael R. Ward (Reg No. 38,651)
E. Thomas Wheelock (Reg No. 28,825)	Todd W. Wight (Reg No. 45,218)
Frank Wu (Reg No. 41,386)	David T. Yang (Reg No. 44,415)
Peter J. Yim (Reg No. 44,417)	George C. Yu (Reg No. 44,418)
Karen R. Zachow (Reg No. 46,332)	

all of Morrison & Foerster LLP, 425 Market Street, San Francisco, California 94105-2482, telephone: (415) 268-7000, said appointment to be to the exclusion of the inventors and their attorneys in accordance with the provisions of 37 C.F.R. § 3.71 provided that if any one of said attorneys or agents ceases being affiliated with the law firm of Morrison & Foerster as partner, employee or of counsel, such attorney's or agent's appointment as attorney or agent and all powers derived therefrom shall terminate on the date such attorney or agent ceases being so affiliated.

The undersigned (whose title is provided below) is empowered to sign this document on behalf of ADVANTEST CORPORATION.

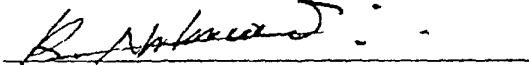
Please direct all communications relative to this application to:

Ararat Kapouytian
Morrison & Foerster LLP
425 Market Street
San Francisco, California 94105-2482

Please direct all telephone communications to Ararat Kapouytian at (415) 268-6147.

ADVANTEST CORPORATION
a corporation of JAPAN

Dated: Sep. 19, 2001


Name: Koji Nakagomi
Title: Manager of Intellectual Property Dept.
Address: Asahicho 1-32-1
Nerima-ku, Tokyo 179-0071
JAPAN

1572429

ASSIGNMENT

Akihiko Ito, a resident of Hanyu-shi, Saitama, Japan, herein referred to as ASSIGNOR, is inventor of a new invention entitled "Semiconductor Device Testing Apparatus and a Test Tray for Use in the Testing Apparatus" by Akihiko Ito, Yoshihito Kobayashi, Yoshiyuki Masuo and Tsuyoshi Yamashita, for which he has filed an international patent application under the Patent Cooperation treaty, such invention being described in and identified by international application number PCT/JP 98/02979 filed July 2, 1998.

ADVANTEST CORPORATION, a corporation of Japan, having a place of business at 32-1, Asahicho 1-chome, Nerima-ku, Tokyo 179-0071, Japan, hereinafter referred to as ASSIGNEE, is desirous of acquiring the entire right, title and interest in and to said application and said invention, in and to any and all improvements relating to said invention, and in and to each and every Patent thereon, when granted in the United States.

In consideration of the employment of ASSIGNOR by ASSIGNEE and for other good and valuable consideration:

1. ASSIGNOR sells, assigns, transfers and conveys unto ASSIGNEE, the entire right, title and interest:
 - (a) in and to said invention, improvements, application, and each and every additional application filed in the United States, which additional application is a division, substitution, continuation or continuation-in-part of or is based on said application or which additional application relates to said invention and improvements;
 - (b) all priority rights associated with the filing of each and every such application, and
 - (c) in and to each and every Patent on said invention and improvements that may be granted, including each and every Patent that may be granted on any of the applications referred to in sub-section (a), and in and to each and every reissue, re-examination or extension of each and every such Patent.
2. ASSIGNOR warrants, covenants and represents the fact that he has not heretofore granted any license, right or privilege in the United States in respect to said invention or said application to any other, or in any other way encumbered the same, and that he has the full right to sell, assign, transfer and convey, free of all licenses and encumbrances, the entire interest hereby assigned.
3. ASSIGNOR covenants that at the request and expense of ASSIGNEE he will promptly execute all papers necessary or desirable to perfect ownership of said invention, applications and said each and every Patent to ASSIGNEE, and execute all oaths, declarations and other papers necessary or desirable for prosecuting said applications, for use in interference proceedings involving said invention, applications and each and every Patent, for use in opposition proceedings involving said invention, applications and each and every Patent, for refiling said applications, for filing of divisional, substitution,

ADVANTEST CORPORATION

continuation or continuation-in-part applications deemed necessary or desirable by ASSIGNEE, or for reissuance or re-examination of said each and every Patent. ASSIGNOR further covenants and agrees that at the expense and request of ASSIGNEE, he will promptly assist ASSIGNEE in interference and opposition proceedings involving said invention, applications and each and every Patent, and in litigation involving said invention, applications and each and every Patent, and will assist in the ascertainment of facts and the production of evidence relating to said invention, applications and each and every Patent.

4. The terms, covenants and provisions of this assignment shall inure to the benefit of ASSIGNEE, its successors, assigns and other legal representatives, and shall be binding upon ASSIGNOR, his heirs, legal representatives and assigns.



ASSIGNOR

AKIHIKO ITO

February 2, 1999

Date

ASSIGNMENT

Yoshihito Kobayashi, a resident of Gyoda-shi, Saitama, Japan, herein referred to as ASSIGNOR, is inventor of a new invention entitled "Semiconductor Device Testing Apparatus and a Test Tray for Use in the Testing Apparatus" by Akihiko Ito, Yoshihito Kobayashi, Yoshiyuki Masuo and Tsuyoshi Yamashita, for which he has filed an international patent application under the Patent Cooperation treaty, such invention being described in and identified by international application number PCT/JP 98/02979 filed July 2, 1998.

ADVANTEST CORPORATION, a corporation of Japan, having a place of business at 32-1, Asahicho 1-chome, Nerima-ku, Tokyo 179-0071, Japan, hereinafter referred to as ASSIGNEE, is desirous of acquiring the entire right, title and interest in and to said application and said invention, in and to any and all improvements relating to said invention, and in and to each and every Patent thereon, when granted in the United States.

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 - (a) in and to said invention, improvements, application, and each and every additional application filed in the United States, which additional application is a division, substitution, continuation or continuation-in-part of or is based on said application or which additional application relates to said invention and improvements;
 - (b) all priority rights associated with the filing of each and every such application, and
 - (c) in and to each and every Patent on said invention and improvements that may be granted, including each and every Patent that may be granted on any of the applications referred to in sub-section (a), and in and to each and every reissue, re-examination or extension of each and every such Patent.
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ASSIGNOR
YOSHIHITO KOBAYASHI

February 2, 1999

Date

137 JAD 5/5

ASSIGNMENT

Yoshiyuki Masuo, a resident of Gyoda-shi, Saitama, Japan, herein referred to as ASSIGNOR, is inventor of a new invention entitled "Semiconductor Device Testing Apparatus and a Test Tray for Use in the Testing Apparatus" by Akihiko Ito, Yoshihito Kobayashi, Yoshiyuki Masuo and Tsuyoshi Yamashita, for which he has filed an international patent application under the Patent Cooperation treaty, such invention being described in and identified by international application number PCT/JP 98/02979 filed July 2, 1998.

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 - (b) all priority rights associated with the filing of each and every such application, and
 - (c) in and to each and every Patent on said invention and improvements that may be granted, including each and every Patent that may be granted on any of the applications referred to in sub-section (a), and in and to each and every reissue, re-examination or extension of each and every such Patent.
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Yoshiyuki Masuo

ASSIGNOR

YOSHIYUKI MASUO

February 2, 1999

Date

207136
7-23-98

ASSIGNMENT

Tsuyoshi Yamashita, a resident of Ora-gun, Gunma, Japan, herein referred to as ASSIGNOR, is inventor of a new invention entitled "Semiconductor Device Testing Apparatus and a Test Tray for Use in the Testing Apparatus" by Akihiko Ito, Yoshihito Kobayashi, Yoshiyuki Masuo and Tsuyoshi Yamashita, for which he has filed an international patent application under the Patent Cooperation treaty, such invention being described in and identified by international application number PCT/JP 98/02979 filed July 2, 1998.

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 - (b) all priority rights associated with the filing of each and every such application, and
 - (c) in and to each and every Patent on said invention and improvements that may be granted, including each and every Patent that may be granted on any of the applications referred to in sub-section (a), and in and to each and every reissue, re-examination or extension of each and every such Patent.
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continuation or continuation-in-part applications deemed necessary or desirable by ASSIGNEE, or for reissuance or re-examination of said each and every Patent. ASSIGNOR further covenants and agrees that at the expense and request of ASSIGNEE, he will promptly assist ASSIGNEE in interference and opposition proceedings involving said invention, applications and each and every Patent, and in litigation involving said invention, applications and each and every Patent, and will assist in the ascertainment of facts and the production of evidence relating to said invention, applications and each and every Patent.

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Tsuyoshi Yamashita
ASSIGNOR
TSUYOSHI YAMASHITA

February 2, 1999

Date